

## Programmable HYBRID Video Signal Generator

**VG-830**Contact us: ASTRODESIGN, Inc. <http://www.astrodesign.co.jp> / ASTROSYSTEMS, Inc. <http://www.astro-systems.com>

## 300MHz QXGA Programmable HYBRID(digital/analog) video signal generator ideal for FPD Technology today and tomorrow...

VG-830 presents users with the various capabilities often sought in an all in one type of video test equipment. Supporting 4-channels of Digital Parallel, 4-channels of LVDS and 1-channel of DVI it makes this video generator an essential tool to have. Additionally this video generator offers traditional interface outputs such as VBS and S-Video for applications dealing with NTSC, PAL and SECAM. Ideally this video generator is highly recommended for use in an R&D or Production environment. Because it is so flexible and adaptable, we can assure you that even at six or seven generations down the road you will still have use for this video generator. Fully programmable supports up to 300MHz Pixel Clock and a high resolution of QXGA.

### Features:

#### WIDEBAND CLOCK RANGE

The output range is between 5 to 300MHz at a QXGA resolution ideal for testing next generation FPD systems.

#### CONFIGURABLE OUTPUT SLOTS FOR OPTIONAL INTERFACE BOARDS

User may choose from any type of configuration of Digital Parallel, LVDS, DM, VBS and S-Video as the optional interface configurations. User must specify what type of output to be installed before unit is actually shipped from the factory.

#### FLEXIBLE TEST RECORD CONTROL VIA LAN

Through the LAN interface users can save the test record. In addition to normal contents, operator key step checking the current position of unit by linking the IP address, such customization, too.

Ethernet is adopted for external interface

The Ethernet port is provided as an essential tool for performing batch check applications

where multiple lines or systems require immediate attention in the production process. This feature alone is a highly useable feature because it saves time and it ensures efficiency.

Various types of test timing and pattern which PC base unit does not support. All timing data can be edited in a 1-dot/ 1-Line Step. Essentially this type of editing adds even more value to normal FPD panel test patterns like Ichimatsu, Color and Cursor patterns ideal for defective pixel checking applications. Other useful patterns like a 24bit true color bitmap image with scroll and flickering for testing react speed check can be programmed for even greater flexibility.

Split screen display function(1/2, 1/4 mode) and programmable data mapping mode. Per the options described in the above, if you utilize the capability of the 2 or 4-channel LVDS board you can output a special timing that will then split the display area of a panel to emulate a 1/2 or 1/4 mode which is suitable for very high resolutions. The LVDS data mapping can be edited per your application requirements plus there are other essential features that make this editing process a flexible aspect for any given test display device.



## specifications

Output Specifications		
Dot Clock Frequency Digital Output	Parallel x 4ch	100.00KHz to 300.000MHz / 10KHz Step
	LVDS x 4ch	20.000MHz to 300.000MHz / 10KHz Step
	DVI x 1ch	25.000MHz to 300.000MHz / 10KHz Step
	Analog Output	
	RGB/YpbPr x 1ch	5.000MHz to 300.000MHz / 10KHz Step
	VBS and S-Video x each 1ch	NTSC / PAL / SECAM
Horizontal Timing		10.00 to 300.00KHz (Maximum 8192 dots)
Vertical Timing		10.00 to 200.00Hz (Maximum 8192 lines)
Video Memory		4k x 4k x 24bits / one of 16.77 million colors
Storage Media	PC ATA Card	Compact Flash card with adapter / 64MB standard (max. 256MB)
External Interface		RS-232C / Remote / LAN x each 1ch
		LAN : 10/100BASE-T/TX
		Remote Box (Optional RB-1848, RB-614C, RB-649)
General specifications		
Voltage		AC 100 to 120V/200 to 240V 50/60Hz
Operating temperature range		+5 to 40
Operating humidity range		30 ~ 80 RH (no condensation)
Dimensions		155(W) 360(H) 320(D)mm
Weight		Approx. 8.0kg

### Rear View

VG-830

